

**B V V Sangha's**  
**Amruta Institute of Engineering and Management Sciences**  
**Department of ECE, III SEMSTER 2019**

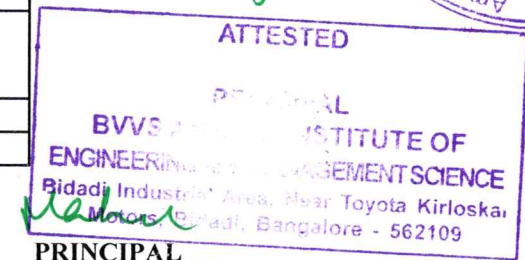
Day/ Time	8.30-9.25	9.25-10.20	10.20-10.30	10.30-11.25	11.25-12.20	12.20-1.00	1.00-1.55	1.55-2.50	2.50-3.45
Mon	M3	NT	TEA BREAK	PE&I	COA	LUNCH BREAK	ED&I LAB		
Tue	SOFT SKILLS			ED	NT		M3	PE&I	DSD
Wed	M3	DSD		COA	PE&I		NT	ED	KANNADA
Thu	M3	ED		DSD	COA		DSD LAB		
Fri	DSD	PE&I		NT	ED		COA	DSD	DA
Sat	ED	COA		M3	NT		PE&I	DIP MATHS	SWACH BHARATH

Code	Subject name	Staff Intial	Staff Name
18MAT31	MATHEMATICS-III	SHOBHA	SHOBHA
18EC32	NETWORK THEORY	GB	GEETHA B
18EC33	ELECTRONICS DEVICES	AN	ANITHA N
18EC34	DIGITAL SYSTEM DESIGN	VP	VEERESH PATIL
18EC35	POWER ELECTRONICS AND INST	RS	RASHMI S
18EC36	COMPUTER ORGANIZATION & ARCH	MS	MADHUSUDHAN S
18ECL37	ED LAB	AN,CRD	ANITHA N, CHITHARANJAN DAS
18ECL38	DSD LAB	VP,AN	VEERESH PATIL,ANITHA N

*[Signature]*  
COORDINATOR

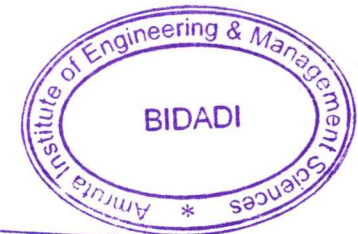
*[Signature]*  
HOD

Head of the Department  
 Dept. of Electronics & Communication Engg  
 Amruta Institute of Engineering &  
 Management Sciences  
 Bidadi, Off Mysore Road, Bangalore - 562 109



PRINCIPAL

BVVS Amruta Institute of Engineering & Management Sciences  
 Bidadi Industrial Area, Near Toyota Kirloskar Motors  
 Bidadi, Bangalore-562 109.




**B V V Sangha's**  
**Amruta Institute of Engineering and Management Sciences**  
**Department of ECE, V SEMSTER 2019**

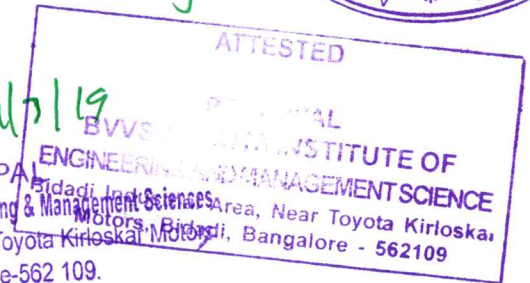
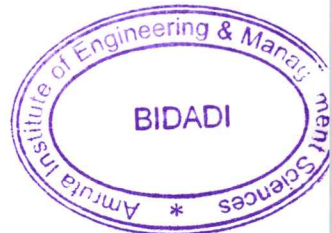
Day/ Time	8.30-9.25	9.25-10.20	10.20-10.40	10.40-11.35	11.35-12.30	12.30-1.00	1.00-1.55	1.55-2.50	2.50-3.45
MON	DSP	OOPS	TEA BREAK	ITC	M&E	LUNCH BREAK	VHDL	OS	LIBRARY
TUE	OS	VHDL		ITC	DSP		DSP LAB		
WED	ITC	M&E		OOPS	VHDL		HDL LAB		
THU	ITC	DSP		VHDL	OOPS		M&E	OS	ITC
FRI	OOPS	M&E		OS	DSP		ITC	M&E	DA
SAT	VHDL	OOPS		M&E	OS		DSP	SWACH BHARATH	

Code	Subject name	Staff Intial	Staff Name
17ES51	MANAGEMENT & ENTRENEUR	CRD	CHITHARANJAN DAS
17EC52	DIGITAL SIGNAL PROCESSING	MS	MADHUSUDHAN S
17EC53	VERILOG HDL	AKK	ANILKUMAR K
17EC54	INFROMATION THEORY & COD	VC	VIJAYALAKSHMI C
17EC553	OPERATING SYSTEM	NF	
17EC562	OBJECT ORIENTED	CSF	
17ECL57	HDL LAB	AKK+CK	ANILKUMAR K + CHETHANA KUMARA
17ECL58	DSP LAB	MS+NF	MADHUSUDHAN S +

  
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 March 31/19  
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**B V V Sangha's**  
**Amruta Institute of Engineering and Management Sciences**  
**Department of ECE, VII SEMSTER 2019**

Day/ Time	8.30-9.25	9.25-10.20	10.20-10.30	10.30-11.25	11.25-12.20	12.20-1.00	1.00-1.55	1.55-2.50	2.50-3.45
MON	M&A	IOT	TEA BREAK	DIP	MM	LUNCH BREAK	VLSI LAB		
TUE	MM	PE		SOFT SKILLS			PROJECT PHASE I		
WED	PE	M&A		DIP	IOT		MM	LIBRARY	
THU	IOT	MM		DIP	PE		ADC LAB		
FRI	DIP	M&A		IOT	PE		M&A	DA	DA
SAT	PE	DIP		M&A	MM		IOT	SWACH BHARATH	

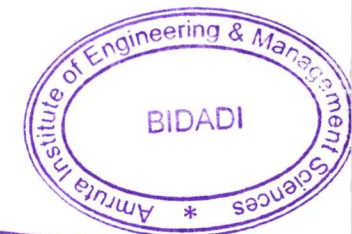
Code	Subject name	Staff Intial	Staff Name
15EC71	MICROWAVE AND AN	AKK	ANILKUMAR K
15EC72	DIGITAL IMAGE PRO	NF	
15EC73	POWER ELECTRONIC	RS	RASHMI S
15EC741	MULTIMEDIA	CK	CHETANA KUMARA
15EC752	IOT	VP	VEERESH PATIL
15ECL76	ADC LAB	CK+AKK	CHETANA KUMARA+ ANILKUMAR
15ECL77	VLSI LAB	MS+NF	MADHUSUDHAN S
17ECP78	PROJECT PHASE I	MS	MADHUSUDHAN S

*[Signature]*  
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**Department of Electronics and Communication Engineering**  
**TIME TABLE EVEN SEM -2019**

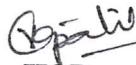
**IV SEMESTER**


Days	8.30-9.25	9.25-10.20	10.20 - 10.40	10.40- 11.35	11.35-12.30	12.30 - 1.00	1.00-1.55	1.55-2.50	2.50-3.45
MON	M4	LIC	B R E A K	MP	PCS	B R E A K	SS	CS	SC
TUE	PCS	MP		M4	LIC		← MP LAB →		
WED	MP	SS		M4	CS		LIC	SS	CPH
THU	LIC	PCS		M4	CS		← LIC LAB →		
FRI	M4	CS		SS	PCS		MP	LIC	Dept. activity
SAT	CS	PCS		M4	MP		SS		

SC=Smart Class

Code	Subject name	Staff Intial	Staff Name
17MAT41	Engg mathematics-IV	SH	Ms.Shobha
17EC42	Microprocessor	VC	Mrs.Vijayalakshmi C
17EC43	Control systems	AN	Mrs.AnithaN
17EC44	Signals and systems	MS	Mr.Madhusudhan S
17EC45	Principle of comm.system	CKS	Mr.Chethana kumar S
17EC46	Linear integrated circuits	RS	Ms.Rashmi S
17CPH49	Constitution of India,Professional Ethics& Human rights	....	Mr.Chowdaiah
17ECL47	Microprocessor LAB	MS+VC	Mr.Madhusudhan S+Mrs.Vijayalakshmi C
17ECL48	Linear IC's and Comm. LAB	AN+CKR	Mrs.AnithaN +Chaitanya Kumar R

  
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**TIME TABLE EVEN SEM -2019**

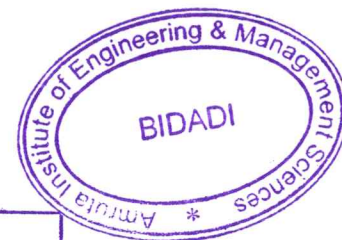
**VI SEMESTER**

Days	8.30-9.25	9.25-10.20	10.20 - 10.40	10.40- 11.35	11.35-12.30	12.30 - 1.00	1.00-1.55	1.55-2.50	2.50-3.45
MON	DC	ARM	B R E A K	DSDV	VLSI	B R E A K	CCN		SC
TUE	ARM	DSDV		VLSI	CCN		CMC	DC	SC
WED	VLSI	DC		ARM	CCN		← ARM LAB →		
THU	CMC	ARM		DC	DSDV		← CCN LAB →		
FRI	CCN	CMC		VLSI	DC		DSDV	CMC	Dept. activity
SAT	DSDV	CCN		ARM	CMC		VLSI		

SC=Smart Class

Code	Subject name	Staff Intial	Staff Name
15EC61	Digital Communication	CKR	Mr.Chaitanya Kumar R
15EC62	ARM & Embedded	AKK	Mr.AnilKumar
15EC63	VLSI Design	CRD	Mr.Chittranjan Das
15EC64	Computer Comm.Networks	VC	Mrs.Vijayalakshmi C
15EC651	Cellular Mobile comm.	VP	Dr.Veeresh Patil
15EC663	Digital system design using verilog	MS	Mr.Madhusudhan S
15ECL67	Embedded Controller lab	AKK+CKS	Mr.AnilKumar + Mr.Chethana Kumar S
15ECL68	CN lab	CKS+AKK	Mr.Chethana Kumar S + Mr.AnilKumar

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TIME TABLE CO-ORDINATOR

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 HOD

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**TIME TABLE EVEN SEM -2019**

18-19

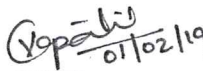
**VIII SEMESTER**

Days	8.30-9.25	9.25-10.20	10.20 - 10.40	10.40- 11.35	11.35- 12.30	12.30 - 1.00	1.00-1.55	1.55-2.50	2.50-3.45		
MON	FON	WC	B R E A K	NS	WC	B R E A K	← Seminar →				
TUE	WC	NS		FON	NS		SC				
WED	NS	FON		WC	FON		Seminar		Dept. activity		
THU	PROJECT WORK			PROJECT WORK			PROJECT WORK				
FRI	PROJECT WORK			PROJECT WORK							
SAT	PROJECT WORK			PROJECT WORK							

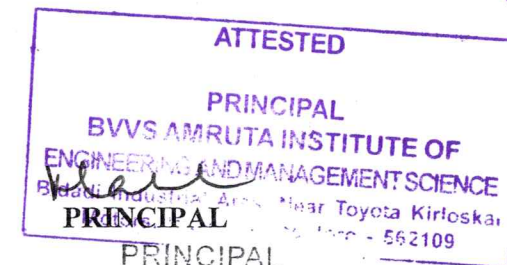
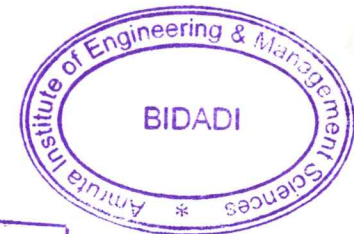
SC=Smart Class

Code	Subject name	Staff Intial	Staff Name
15EC81	Wireless Cellular & LTE 4G	VP	Dr.Veeresh Patil
15EC82	Fibre Optics & Networks	CKS	Mr.Chethana kumar S
15EC835	Network & Cyber security	AN	Mrs.AnithaN
15ECP858	Project Work	AN	Mrs.AnithaN
15ECS86	Seminar	AN	Mrs.AnithaN

  
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